

THOMSON

DELPHION

RESEARCH

PRODUCTS

INSIDE DELPHION

Log Out

Work Files

Saved Searches

My Account


Search: Quick/Number Boolean Advanced Derive

The Delphion Integrated View: INPADOC Record

Get Now: ☒ PDF | [More choices...](#)

Tools: Add to Work File: | Create new Work File

View: Jump to: Top

 Email

? Title: **TW0448407B: HARDWARE ACCELERATOR FOR AN OBJECT-ORIENTED PROGRAMMING LANGUAGE**

? Country: **TW** Taiwan

? Kind: **B** Patent

? Inventor: **MINAMI, JOHN SHIGETO**; United States of America
KOYAMA, RYO; United States of America
POFF, THOMAS C.; United States of America

? Assignee: **IREADY CORPORATION** United States of America
[News, Profiles, Stocks and More about this company](#)



[Hi](#)
[Re](#)

? Published / Filed: **2001-08-01 / 1999-11-18**

? Application Number: **TW1999088120193**

? IPC Code: **G06F 9/45; G06F 17/00;**

? ECLA Code: **None**

? Priority Number: **1999-11-18 TW1999088120193**

? Abstract: A method and apparatus for accelerating an object-oriented programming language are provided at a hardware gate level. In a Java-compliant embodiment, a Java Application framework is implemented in hardware. The Java. AWT, Java. NET, and Java. IO application frameworks are supported in the preferred embodiment of the invention. Application framework classes are stored as libraries in a shared memory. Instances and methods of supported application framework classes that are executed by a Java program are offloaded to a hardware object management system. A software stub is provided as an interface between the hardware object management system and the central processing unit. Additional application frameworks can be supported by modifying or replacing the software stub. Hardware object management system requests are executed by an application framework-specific hardware accelerator. Application framework classes are retrieved from the shared memory as needed, and executed instructions are stored to the shared memory to be accessed by the central processing unit. Central processing unit processing of non-supported application framework instructions is continued during hardware accelerator execution of hardware object management system requests.

? Family:

PDF	Publication	Pub. Date	Filed	Title
<input checked="" type="checkbox"/>	TW0448407B	2001-08-01	1999-11-18	HARDWARE ACCELERATOR FOR AN OBJECT-ORIENTED PROGRAMMING LANGUAGE

1 family members shown above

Other Abstract
Info:

None



Nominate this for the Gall

Copyright © 1997-2004
The Thomson Corporation

[Subscriptions](#) | [Web Seminars](#) | [Privacy](#) | [Terms & Conditions](#) | [Site Map](#) | [Contact U](#)